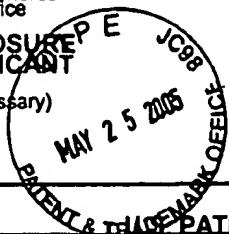


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Tetsuya IKEDA, et al.

FILING DATE

May 31, 2001

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PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
M.N.	5 5 0 2 6 9 6	03/1996	Yoon et al.			

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
M.N.	0 0 3 5 1 0 1	06/2000	WO				
M.N.	0 6 6 0 5 5 8	06/1995	EP				

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

M.N.	Supplementary European Search Report dated February 24, 2005.
M.N.	"A New Low-Complexity Turbo Code Interleaver Employing Linear Congruential Sequences," TR45.5.3.1/98.12.08.08, XP002237239 pages 1-14, Dec. 8, 1998.
M.N.	Nortel Networks, NTTDoCoMo, "Prime Interleaver Complexity Analysis," TDOC R1-99-0513 of 3GPP TSG RAN WG 1, Meeting 4, XP002315002, pages 1-10, Apr. 18, 1999.
M.N.	Motorola, "A Proposal for Turbo Code Interleaving," TDOC R1-99-0239 of 3GPP TSG RAN WG 1, Meeting 3, XP002315003, pages 1-8, Mar. 22, 1999.
M.N.	Eroz, M. et al., "On the Design of Prunable Interleavers for Turbo Codes," Proc. of Vehicular Technology Conference, Houston, TX, vol. 2, XP010342111, ISBN 0-7803-5565-2, pages 1669-1673, May 16, 1999.

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